

5-22-01
\$2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/648,585
Priority Filing Date August 25, 2000
Inventor Belford T. Coursey
Assignee Micron Technology, Inc.
Priority Group Art Unit 2813
Priority Examiner Y. Huynh
Attorney's Docket No. MI22-1660
Title: Memory Circuitry, and Dynamic Random Access Memory Circuitry (As Amended)

U.S. PTO
09/810595
03/15/01

EL465780142US

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09/648,585, filed August 25, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned.

37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 3-14-01

Attorney: 
Mark S. Matkin
Reg. No. 32,268